DIGITUS®

Application Report
PSA-3402 – December 2024

SIFOs 802.3bt Conformance Report, v 5.4.10, DN-95109

1 Sifos Technologies

Sifos Technologies provides a one-box solution to facilitate complete testing and analysis of Power Sourcing Equipment (PSE) behaviors and overall compliance to the IEEE 802.3bt specification. The PSE Conformance Test Suite serves as a virtual industry standard for PSE specification compliance. SIFOS test coverage exceeds 95% of 802.3at PSE PICS

2 Enhanced Test Conditions

In additional to just running the basic conformance testing on an individual port and to better recreate the system-level environment, ASSMANN individually tests all ports of its PSE controller devices while having the background ports running at various PoE-application conditions. ASSMANN further, repeats testing conditions over extended periods of time to ensure performance is consistent after multiple hours, days and/or continued operation.

PSE Conformance Test Suite

December 27 2024	3:02 PM
Port Count.....	1
Loop Count.....	1

PSE Tested: **Results Type-4**

Sifos Technologies

802.3bt 4Pr Conformance Report

34020093A *version* *5.4.10*

PSE Type: **4 MDI-X+MDI** *report version 5.4.02*

Safety Index*: **100%** **Interop Index*:** **100%**

Error Log: None

Chassis ID: 192.168.221.105	PSA-3402 Ports					Low	P/F	High	P/F
TestLoop: 1	1-1	UNITS	Min	Max	Average	Limit		Limit	
Test: det_v									
Open_Circuit_Voc_A=	10.4	volts	10.4	10.4	10.4	0	Pass	30	Pass
Open_Circuit_Voc_B=	14	volts	14	14	14	0	Pass	30	Pass
Backoff_Voltage_A=	0.8	volts	0.8	0.8	0.8	0	Pass	2.8	Pass
Backoff_Voltage_B=	0.9	volts	0.9	0.9	0.9	0	Pass	2.8	Pass
Backoff_Voltage_Ss=	0.9	volts	0.9	0.9	0.9	0	Pass	2.8	Pass
Max_Det_Step_V_A=	6.98	volts	6.98	6.98	6.98	3.8	Pass	10	Pass
Max_Det_Step_V_B=	7.08	volts	7.08	7.08	7.08	3.8	Pass	10	Pass
Min_Det_Step_V_A=	3.65	volts	3.65	3.65	3.65	2.8	Pass	9	Pass
Min_Det_Step_V_B=	3.71	volts	3.71	3.71	3.71	2.8	Pass	9	Pass
Det_Step_Changes_A=	1	****	1	1	1	1	Pass	9	Pass
Det_Step_Changes_B=	1	****	1	1	1	1	Pass	9	Pass
Min_Step_DV_A=	1.7	volts	1.7	1.7	1.7	1	Pass	7.2	Pass
Min_Step_DV_B=	1.73	volts	1.73	1.73	1.73	1	Pass	7.2	Pass
Pre-Det_CC_Step_V_A=	3.64	volts	3.64	3.64	3.64	0	Pass	10	Pass
Pre-Det_CC_Step_V_B=	0	volts	0	0	0	0	Pass	10	Pass
Test: det_cc									
Presumed_CC_DET_SEQ=	1	****	1	1	1	0	Pass	3	Pass
Conn_Chk_SS_V_A=	9.01	volts	9.01	9.01	9.01	2.8	Pass	10	Pass
Conn_Chk_SS_V_B=	9.06	volts	9.06	9.06	9.06	2.8	Pass	10	Pass
Conn_Chk_DS_V_A=	3.71	volts	3.71	3.71	3.71	2.8	Pass	10	Pass
Conn_Chk_DS_V_B=	6.35	volts	6.35	6.35	6.35	2.8	Pass	10	Pass
High_Signature_CC_A=	1	****	1	1	1	1	Pass	1	Pass
High_Signature_CC_B=	1	****	1	1	1	1	Pass	1	Pass
4Pair_Start_Fail=	0	****	0	0	0	0	Pass	0	Pass
Test: det_i									
Isc_Init_A=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Init_B=	0.23	mA	0.23	0.23	0.23	0	Pass	5	Pass
Isc_Det_A=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Det_B=	0.23	mA	0.23	0.23	0.23	0	Pass	5	Pass
Det_Slew_A=	0.0044	V/usec	0.0044	0.0044	0.0044	0	Pass	0.1	Pass
Det_Slew_B=	0.0046	V/usec	0.0046	0.0046	0.0046	0	Pass	0.1	Pass
Test: det_range									
Rgood_Max_Single=	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood_Min_Single=	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Single=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rgood_Max_Dual_A=	30	Kohm	30	30	30	27	Pass	32	Pass
Rgood_Max_Dual_B=	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood_Min_Dual_A=	18	Kohm	18	18	18	16	Pass	19	Pass
Rgood_Min_Dual_B=	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Dual_A=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass

Enhanced Test Condition

Cgood_Max_Dual_B=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Test: det_time									
Detect_Time_Tdet_A=	371.1	msec	371.1	371.1	371.1	0	Pass	500	Pass
Detect_Time_Tdet_B=	375	msec	375	375	375	0	Pass	500	Pass
Backoff_Time_SS=	1062.5	msec	1062.5	1062.5	1062.5	15	Pass	9999	Pass
Backoff_Time_DS=	1472.6	msec	1472.6	1472.6	1472.6	15	Pass	9999	Pass
Det2Det_Time=	230.5	msec	230.5	230.5	230.5	0	Pass	400	Pass
Test: det_rsource									
PSE_Detect_Source=	1	****	1	1	1	0	Pass	1	Pass
PSE_Source_Zout_A=	300	Kohm	300	300	300	45	Pass	300	Pass
PSE_Source_Zout_B=	300	Kohm	300	300	300	45	Pass	300	Pass
Test: cc_response									
Single_Sig_Response=	1	****	1	1	1	1	Pass	1	Pass
Dual_Sig_Response=	1	****	1	1	1	1	Pass	1	Pass
2Pair_PD_A=	1	****	1	1	1	0	Pass	1	Pass
2Pair_PD_B=	1	****	1	1	1	0	Pass	1	Pass
Test: class_v									
Vclass_max_SS=	18.7	volts	18.7	18.7	18.7	15.5	Pass	20.5	Pass
Vclass_min_SS=	18.4	volts	18.4	18.4	18.4	15.5	Pass	20.5	Pass
Vmark_SS=	8.9	volts	8.9	8.9	8.9	7	Pass	10	Pass
Vreset_SS=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vclass_max_DSA=	18.7	volts	18.7	18.7	18.7	15.5	Pass	20.5	Pass
Vclass_max_DSB=	18.7	volts	18.7	18.7	18.7	15.5	Pass	20.5	Pass
Vclass_min_DSA=	18.4	volts	18.4	18.4	18.4	15.5	Pass	20.5	Pass
Vclass_min_DSB=	18.3	volts	18.3	18.3	18.3	15.5	Pass	20.5	Pass
Vmark_DSA=	8.8	volts	8.8	8.8	8.8	7	Pass	10	Pass
Vmark_DSB=	8.9	volts	8.9	8.9	8.9	7	Pass	10	Pass
Vreset_DSA=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vreset_DSB=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Test: class_time									
Class_Probe_SS=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_7_SS=	5	Events	5	5	5	1	Pass	5	Pass
Long_EV1_Time_SS=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Max_Class_EV_Time_SS=	13.6	msec	13.6	13.6	13.6	6	Pass	20	Pass
Min_Mark_EV_Time_SS=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_SS=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Min_Mark_EV_LowI_SS=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Final_Mark_EV_Time_SS=	121.1	msec	121.1	121.1	121.1	6	Pass	256	Pass
Cl_Prb_Reset_Time_SS=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DA=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DA=	4	Events	4	4	4	1	Pass	4	Pass
Long_EV1_Time_DA=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_DA=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Max_Class_EV_Time_DA=	13.6	msec	13.6	13.6	13.6	6	Pass	20	Pass
Min_Mark_EV_Time_DA=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_DA=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Final_Mark_EV_Time_DA=	123.1	msec	123.1	123.1	123.1	6	Pass	256	Pass
Cl_Prb_Reset_Time_DA=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DB=	0	****	0	0	0	0	Pass	1	Pass

EV_Count_5D_DB=	4	Events	4	4	4	1	Pass	4	Pass
Long_EV1_Time_DB=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_DB=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Max_Class_EV_Time_DB=	13.6	msec	13.6	13.6	13.6	6	Pass	20	Pass
Min_Mark_EV_Time_DB=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_DB=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Final_Mark_EV_Time_DB=	123.1	msec	123.1	123.1	123.1	6	Pass	256	Pass
Cl_Prb_Reset_Time_DB=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Test: class_response									
Class_3_Count=	1	****	1	1	1	1	Pass	1	Pass
Class_4_Count=	3	****	3	3	3	1	Pass	3	Pass
Class_5_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_6_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_7_Count=	5	****	5	5	5	1	Pass	5	Pass
Class_8_Count=	5	****	5	5	5	1	Pass	5	Pass
Class_2D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_2D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_3D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_3D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_5D_Count_A=	4	****	4	4	4	1	Pass	4	Pass
Class_5D_Count_B=	4	****	4	4	4	1	Pass	4	Pass
Max_SS_Class=	8	****	8	8	8	3	Pass	8	Pass
Max_DS_Class=	5	****	5	5	5	1	Pass	5	Pass
Init_Grant_Match=	1	****	1	1	1	1	Pass	1	Pass
2-Pair_Pairset=	0	****	0	0	0	0	Pass	2	Pass
PRI_4pr_Pairset=	12	****	12	12	12	1	Pass	12	Pass
Test: class_err									
Class_Ilim_A=	67.9	mA	67.9	67.9	67.9	51	Pass	100	Pass
Class_Ilim_B=	66.8	mA	66.8	66.8	66.8	51	Pass	100	Pass
Pwr_Cl_52_SS=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_52_DSA=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_52_DSB=	0	****	0	0	0	0	Pass	0	Pass
Class_Reset_SS=	1	****	1	1	1	1	Pass	1	Pass
Class_Reset_DS=	1	****	1	1	1	1	Pass	1	Pass
Mark_Ilim_A=	60	mA	60	60	60	5	Pass	100	Pass
Mark_Ilim_B=	62	mA	62	62	62	5	Pass	100	Pass
Inval_Sig_EV2_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV5_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV2_DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV2_DSB=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_DSB=	0	****	0	0	0	0	Pass	1	Pass
Test: pwrup_time									
Pwr_On_Time_Tpon_SS=	368.4	msec	368.4	368.4	368.4	144	Pass	400	Pass
Pwr_On_Time_Tpon_DSA=	344.9	msec	344.9	344.9	344.9	144	Pass	400	Pass
Pwr_On_Time_Tpon_DSB=	344.9	msec	344.9	344.9	344.9	144	Pass	400	Pass
Pwrup_Rise_Time_A=	137	usec	137	137	137	15	Pass	50000	Pass
Pwrup_Rise_Time_B=	139	usec	139	139	139	15	Pass	50000	Pass

Pwr_Stagger_Time_SS4=	0	msec	0	0	0	-1	Pass	75	Pass
Pwr_Stagger_Time_SS5=	0	msec	0	0	0	0	Pass	75	Pass
Pwr_Stagger_Time_DS=	705.9	msec	705.9	705.9	705.9	0	Pass	1000	Pass
Test: pwrup_inrush									
Iinrush_min_Class_3=	421.3	mA	421.3	421.3	421.3	400	Pass	9999	Pass
Iinrush_min_Class_5=	862.6	mA	862.6	862.6	862.6	400	Pass	9999	Pass
Iinrush_min_Class_7=	861.9	mA	861.9	861.9	861.9	800	Pass	9999	Pass
Iinrush_min_Class_1D_A=	414.1	mA	414.1	414.1	414.1	400	Pass	9999	Pass
Iinrush_min_Class_1D_B=	414.1	mA	414.1	414.1	414.1	400	Pass	9999	Pass
Iinrush_4P_max_Class_3=	446.8	mA	446.8	446.8	446.8	0	Pass	450	Pass
Iinrush_4P_max2_Class_5=	888.7	mA	888.7	888.7	888.7	0	Pass	900	Pass
Iinrush_4P_max2_Class_7=	885.3	mA	885.3	885.3	885.3	0	Pass	900	Pass
Iinrush_2P_max_Class_3=	225	mA	225	225	225	0	Pass	450	Pass
Iinrush_2P_max2_Class_7=	445.8	mA	445.8	445.8	445.8	0	Pass	600	Pass
Iinrush_2p_max_Cl_1D_A=	434.7	mA	434.7	434.7	434.7	0	Pass	450	Pass
Iinrush_2p_max_Cl_1D_B=	434.5	mA	434.5	434.5	434.5	0	Pass	450	Pass
Tinrush_minPr_Class_3=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Tinrush_maxPr_Class_3=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Tinrush_minPr_Class_7=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Tinrush_maxPr_Class_7=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Tinrush_Class_1D_A=	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush_Class_1D_B=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Delay_Inrush_Class_7=	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Delay_Inrush_Class_2D_A=	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Delay_Inrush_Class_2D_B=	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
45ms_Pwr_Stat_Class_7=	1	****	1	1	1	1	Pass	1	Pass
45ms_Pwr_Stat_Class_2D_A=	1	****	1	1	1	1	Pass	1	Pass
45ms_Pwr_Stat_Class_2D_B=	1	****	1	1	1	1	Pass	1	Pass
Vinrush_Class_2D_A=	30.8	volts	30.8	30.8	30.8	30	Pass	60	Pass
Vinrush_Class_2D_B=	30.8	volts	30.8	30.8	30.8	30	Pass	60	Pass
Iinrush_16V_DS3A=	429.75	mA	429.75	429.75	429.8	60	Pass	450	Pass
Iinrush_16V_DS3B=	173	mA	173	173	173	60	Pass	450	Pass
Test: pwron_v									
Vpse_Max_Alt_A=	56.42	V	56.42	56.42	56.42	52	Pass	57	Pass
Vpse_Max_Alt_B=	56.38	V	56.38	56.38	56.38	52	Pass	57	Pass
Vpse_Min_Alt_A=	55.42	V	55.42	55.42	55.42	52	Pass	57	Pass
Vpse_Min_Alt_B=	55.52	V	55.52	55.52	55.52	52	Pass	57	Pass
Vport_PSE_diff=	0	mV	0	0	0	0	Pass	65	Pass
V_ripple_A=	237	mVp-p	237	237	237	0	Pass	500	Pass
V_ripple_B=	242	mVp-p	242	242	242	0	Pass	500	Pass
V_noise_A=	154	mVp-p	154	154	154	0	Pass	200	Pass
V_noise_B=	155	mVp-p	155	155	155	0	Pass	200	Pass
V_trans_A=	55.34	V	55.34	55.34	55.34	52	Pass	57	Pass
V_trans_B=	55.42	V	55.42	55.42	55.42	52	Pass	57	Pass
Test: pwron_pwrcap									
Max_Asgn_Class_SS=	8	****	8	8	8	1	Pass	8	Pass
Pcon_c1=	4.4	W	4.4	4.4	4.4	3.9	Pass	99.9	Pass
Icon_%_c1=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c2=	7.4	W	7.4	7.4	7.4	3.9	Pass	99.9	Pass
Icon_%_c2=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass

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Pcon_c3=	15	W	15	15	15	3.9	Pass	99.9	Pass
Icon_%_c3=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c4=	30.3	W	30.3	30.3	30.3	3.9	Pass	99.9	Pass
Icon_%_c4=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c5=	49.2	W	49.2	49.2	49.2	3.9	Pass	99.9	Pass
Icon_%_c5=	112.1	%	112.1	112.1	112.1	100	Pass	125	Pass
Pcon_c6=	64.6	W	64.6	64.6	64.6	3.9	Pass	99.9	Pass
Icon_%_c6=	112	%	112	112	112	100	Pass	125	Pass
Pcon_c7=	81.2	W	81.2	81.2	81.2	3.9	Pass	99.9	Pass
Icon_%_c7=	111.6	%	111.6	111.6	111.6	100	Pass	125	Pass
Pcon_c8=	96.5	W	96.5	96.5	96.5	3.9	Pass	99.9	Pass
Icon_%_c8=	111.4	%	111.4	111.4	111.4	100	Pass	125	Pass
Type_N_Enable=	1	****	1	1	1	1	Pass	1	Pass
Pclass_2p_c5=	2	****	2	2	2	1	Pass	2	Pass
Max_Asgn_Class_DS=	5	****	5	5	5	1	Pass	5	Pass
Pcon_c1DA=	4.4	W	4.4	4.4	4	3.9	Pass	49.9	Pass
Icon_%_c1DA=	112.6	%	112.6	112.6	112.6	100	Pass	125	Pass
Pcon_c2DB=	7.5	W	7.5	7.5	8	3.9	Pass	49.9	Pass
Icon_%_c2DB=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c3DA=	15.4	W	15.4	15.4	15	3.9	Pass	49.9	Pass
Icon_%_c3DA=	112.3	%	112.3	112.3	112.3	100	Pass	125	Pass
Pcon_c4DB=	32.3	W	32.3	32.3	32	3.9	Pass	49.9	Pass
Icon_%_c4DB=	112	%	112	112	112	100	Pass	125	Pass
Pcon_c5DA=	48.1	W	48.1	48.1	48	3.9	Pass	49.9	Pass
Icon_%_c5DA=	111.3	%	111.3	111.3	111.3	100	Pass	125	Pass
Test: pwron_unbal									
pseP2pUnbal_c4A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c4B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_B=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_2p_max_SSA=	1216.8	mA	1216.8	1216.8	1216.8	0	Pass	1750	Pass
Ilim_2p_max_SSB=	1211.8	mA	1211.8	1211.8	1211.8	0	Pass	1750	Pass
Tlim_SS=	11.72	msec	11.72	11.72	11.7	6	Pass	75	Pass
Ilim_2p_max_DSA=	1270	mA	1270	1270	1270	0	Pass	1750	Pass
Ilim_2p_max_DSB=	1274.6	mA	1274.6	1274.6	1274.6	0	Pass	1750	Pass
Tlim_DSA=	11.72	msec	11.72	11.72	11.7	6	Pass	75	Pass
Tlim_DSB=	11.72	msec	11.72	11.72	11.7	6	Pass	75	Pass
Ilim_min_cAB3=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4=	684	mA	684	684	684	684	Pass	1750	Pass

Max_trans_c4=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5=	580	mA	580	580	580	580	Pass	1750	Pass
Max_trans_c5=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB6=	720	mA	720	720	720	720	Pass	1750	Pass
Max_trans_c6=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB7=	850	mA	850	850	850	850	Pass	1750	Pass
Max_trans_c7=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB8=	1005	mA	1005	1005	1005	1005	Pass	1750	Pass
Max_trans_c8=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB3D=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4D=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5D=	990	mA	990	990	990	990	Pass	1750	Pass
Max_trans_c5D=	1	****	1	1	1	1	Pass	1	Pass
Vtrans_2p_A=	55.48	V	55.48	55.48	55.5	48.4	Pass	57	Pass
Vtrans_2p_B=	55.58	V	55.58	55.58	55.6	48.4	Pass	57	Pass
Iport_max_type4=	0	****	0	0	0	0	Pass	0	Pass
Ilps_type4=	0	****	0	0	0	0	Pass	0	Pass
Test: pwron_overld									
Ipeak_c1=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c3=	56.1	V	56.1	56.1	56.1	52	Pass	57	Pass
Ipeak_5%DC_c3=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c4=	55.85	V	55.85	55.85	55.9	52	Pass	57	Pass
Ipeak_5%DC_c4=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c5=	55.65	V	55.65	55.65	55.7	52	Pass	57	Pass
Ipeak_5%DC_c5=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c6=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c6=	55.42	V	55.42	55.42	55.4	52	Pass	57	Pass
Ipeak_5%DC_c6=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c7=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c7=	55.5	V	55.5	55.5	55.5	52	Pass	57	Pass
Ipeak_5%DC_c7=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c8=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c8=	55.3	V	55.3	55.3	55.3	52	Pass	57	Pass
Ipeak_5%DC_c8=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c1D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_valid									
Ihold_c3=	4	mA	4	4	4	4	Pass	9	Pass
Ihold_2p_c3A=	2	mA	2	2	2	2	Pass	5	Pass
Ihold_2p_c3B=	3	mA	3	3	3	2	Pass	5	Pass
Ihold_c5=	4	mA	4	4	4	4	Pass	14	Pass
Ihold_2p_c5A=	3	mA	3	3	3	2	Pass	7	Pass

Ihold_2p_c5B=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_c7=	4	mA	4	4	4	4	Pass	14	Pass
Ihold_2p_c7A=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c7B=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c2DA=	4	mA	4	4	4	2	Pass	7	Pass
Ihold_2p_c2DB=	4	mA	4	4	4	2	Pass	7	Pass
LP_MPS_Tol_c3=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c5=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c7=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c2D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn									
Tmpdo_c3A=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c3B=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c5A=	363.3	msec	363.3	363.3	363.3	320	Pass	400	Pass
Tmpdo_c5B=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c7A=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c7B=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c2DA=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
4pr_Stat_c2DA=	1	****	1	1	1	0	Pass	1	Pass
Tmpdo_c2DB=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
4pr_Stat_c2DB=	1	****	1	1	1	0	Pass	1	Pass
Test: pwrdn_time									
Turnoff_time_Toff_A=	3.5	msec	3.5	3.5	3.5	0	Pass	500	Pass
Turnoff_time_Toff_B=	3.5	msec	3.5	3.5	3.5	0	Pass	500	Pass
Cout_A=	19.7	nF	19.7	19.7	19.7	0	Pass	520	Pass
Cout_B=	19.9	nF	19.9	19.9	19.9	0	Pass	520	Pass
Output_Rp_A=	74	Kohm	74	74	74	45	Pass	9999	Pass
Output_Rp_B=	73	Kohm	73	73	73	45	Pass	9999	Pass
Idle_time_SS=	1500	msec	1500	1500	1500	15	Pass	9999	Pass
Test: pwrdn_v									
Error_Delay_SS_A=	996	msec	996	996	996	750	Pass	9999	Pass
Error_Delay_SS_B=	996	msec	996	996	996	750	Pass	9999	Pass
Error_Delay_DS_A=	1484	msec	1484	1484	1484	750	Pass	9999	Pass
Error_Delay_DS_B=	1016	msec	1016	1016	1016	750	Pass	9999	Pass
Idle_Voff_SS_A=	1	V	1	1	1	0	Pass	2.8	Pass
Idle_Voff_SS_B=	1	V	1	1	1	0	Pass	2.8	Pass
Idle_Voff_DS_A=	0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass
Idle_Voff_DS_B=	0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass