

SIFOs 802.3at Conformance Report, v 5.4.10, DN-95118

1 Sifos Technologies

Sifos Technologies provides a one-box solution to facilitate complete testing and analysis of Power Sourcing Equipment (PSE) behaviors and overall compliance to the IEEE 802.3at specification. The PSE Conformance Test Suite serves as a virtual industry standard for PSE specification compliance. SIFOS test coverage exceeds 95% of 802.3at PSE PICS

2 Enhanced Test Conditions

In addition to just running the basic conformance testing on an individual port and to better recreate the system-level environment, ASSMANN individually tests all ports of its PSE controller devices while having the background ports running at various PoE-application conditions. ASSMANN further, repeats testing conditions over extended periods of time to ensure performance is consistent after multiple hours, days and/or continued operation.

Enhanced Test Condition

PSE Conformance Test Suite			 802.3bt 4Pr Conformance Report						
November 26 2024 10:17 AM			34020093A version 5.4.10 PSE Type: 4 MDI-X+MDI report version 5.4.02 Safety Index*: 100% Interop Index*: 100% Error Log: None						
Port Count.....	1								
Loop Count.....	1								
PSE Tested: Results Type-4									
Chassis ID: 192.168.221.105	PSA-3402 Ports					Low	P/F	High	P/F
TestLoop: 1	1-1	UNITS	Min	Max	Average	Limit		Limit	
Test: det_v									
Open_Circuit_Voc_A=	13.2	volts	13.2	13.2	13.2	0	Pass	30	Pass
Open_Circuit_Voc_B=	11.8	volts	11.8	11.8	11.8	0	Pass	30	Pass
Backoff_Voltage_A=	0.9	volts	0.9	0.9	0.9	0	Pass	2.8	Pass
Backoff_Voltage_B=	1	volts	1	1	1	0	Pass	2.8	Pass
Backoff_Voltage_Ss=	1	volts	1	1	1	0	Pass	2.8	Pass
Max_Det_Step_V_A=	6.95	volts	6.95	6.95	6.95	3.8	Pass	10	Pass
Max_Det_Step_V_B=	6.96	volts	6.96	6.96	6.96	3.8	Pass	10	Pass
Min_Det_Step_V_A=	3.63	volts	3.63	3.63	3.63	2.8	Pass	9	Pass
Min_Det_Step_V_B=	3.64	volts	3.64	3.64	3.64	2.8	Pass	9	Pass
Det_Step_Changes_A=	1	****	1	1	1	1	Pass	9	Pass
Det_Step_Changes_B=	1	****	1	1	1	1	Pass	9	Pass
Min_Step_DV_A=	1.71	volts	1.71	1.71	1.71	1	Pass	7.2	Pass
Min_Step_DV_B=	1.71	volts	1.71	1.71	1.71	1	Pass	7.2	Pass
Pre-Det_CC_Step_V_A=	3.61	volts	3.61	3.61	3.61	0	Pass	10	Pass
Pre-Det_CC_Step_V_B=	0	volts	0	0	0	0	Pass	10	Pass
Test: det_cc									
Presumed_CC_DET_SEQ=	1	****	1	1	1	0	Pass	3	Pass
Conn_Chk_SS_V_A=	8.8	volts	8.8	8.8	8.8	2.8	Pass	10	Pass
Conn_Chk_SS_V_B=	8.83	volts	8.83	8.83	8.83	2.8	Pass	10	Pass
Conn_Chk_DS_V_A=	3.63	volts	3.63	3.63	3.63	2.8	Pass	10	Pass
Conn_Chk_DS_V_B=	6.21	volts	6.21	6.21	6.21	2.8	Pass	10	Pass
High_Signature_CC_A=	1	****	1	1	1	1	Pass	1	Pass
High_Signature_CC_B=	1	****	1	1	1	1	Pass	1	Pass
4Pair_Start_Fail=	0	****	0	0	0	0	Pass	0	Pass
Test: det_i									
Isc_Init_A=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Init_B=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Det_A=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Det_B=	0.23	mA	0.23	0.23	0.23	0	Pass	5	Pass
Det_Slew_A=	0.0044	V/usec	0.0044	0.0044	0.0044	0	Pass	0.1	Pass
Det_Slew_B=	0.0046	V/usec	0.0046	0.0046	0.0046	0	Pass	0.1	Pass
Test: det_range									
Rgood_Max_Single=	30	Kohm	30	30	30	27	Pass	32	Pass
Rgood_Min_Single=	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Single=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rgood_Max_Dual_A=	30	Kohm	30	30	30	27	Pass	32	Pass
Rgood_Max_Dual_B=	30	Kohm	30	30	30	27	Pass	32	Pass
Rgood_Min_Dual_A=	17	Kohm	17	17	17	16	Pass	19	Pass
Rgood_Min_Dual_B=	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Dual_A=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Cgood_Max_Dual_B=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass

Enhanced Test Condition

Test: det_time									
Detect_Time Tdet_A=	375	msec	375	375	375	0	Pass	500	Pass
Detect_Time Tdet_B=	390.6	msec	390.6	390.6	390.6	0	Pass	500	Pass
Backoff_Time SS=	1046.9	msec	1046.9	1046.9	1046.9	15	Pass	9999	Pass
Backoff_Time DS=	1496.1	msec	1496.1	1496.1	1496.1	15	Pass	9999	Pass
Det2Det_Time=	242.2	msec	242.2	242.2	242.2	0	Pass	400	Pass
Test: det_rsource									
PSE_Detect_Source=	1	****	1	1	1	0	Pass	1	Pass
PSE_Source_Zout_A=	300	Kohm	300	300	300	45	Pass	300	Pass
PSE_Source_Zout_B=	300	Kohm	300	300	300	45	Pass	300	Pass
Test: cc_response									
Single_Sig_Response=	1	****	1	1	1	1	Pass	1	Pass
Dual_Sig_Response=	1	****	1	1	1	1	Pass	1	Pass
2Pair_PD_A=	1	****	1	1	1	0	Pass	1	Pass
2Pair_PD_B=	1	****	1	1	1	0	Pass	1	Pass
Test: class_v									
Vclass_max_SS=	19.2	volts	19.2	19.2	19.2	15.5	Pass	20.5	Pass
Vclass_min_SS=	18.8	volts	18.8	18.8	18.8	15.5	Pass	20.5	Pass
Vmark_SS=	9	volts	9	9	9	7	Pass	10	Pass
Vreset_SS=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vclass_max_DSA=	18.8	volts	18.8	18.8	18.8	15.5	Pass	20.5	Pass
Vclass_max_DSB=	19.2	volts	19.2	19.2	19.2	15.5	Pass	20.5	Pass
Vclass_min_DSA=	18.4	volts	18.4	18.4	18.4	15.5	Pass	20.5	Pass
Vclass_min_DSB=	18.8	volts	18.8	18.8	18.8	15.5	Pass	20.5	Pass
Vmark_DSA=	9	volts	9	9	9	7	Pass	10	Pass
Vmark_DSB=	9	volts	9	9	9	7	Pass	10	Pass
Vreset_DSA=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vreset_DSB=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Test: class_time									
Class_Probe_SS=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_7_SS=	5	Events	5	5	5	1	Pass	5	Pass
Long_EV1_Time_SS=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_SS=	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Max_Class_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Min_Mark_EV_Time_SS=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Min_Mark_EV_LowI_SS=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Final_Mark_EV_Time_SS=	125	msec	125	125	125	6	Pass	256	Pass
Cl_Prb_Reset_Time_SS=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DA=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DA=	4	Events	4	4	4	1	Pass	4	Pass
Long_EV1_Time_DA=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_DA=	9.7	msec	9.7	9.7	9.7	6	Pass	20	Pass
Max_Class_EV_Time_DA=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Min_Mark_EV_Time_DA=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Max_Mark_EV_Time_DA=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Final_Mark_EV_Time_DA=	125	msec	125	125	125	6	Pass	256	Pass
Cl_Prb_Reset_Time_DA=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DB=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DB=	4	Events	4	4	4	1	Pass	4	Pass
Long_EV1_Time_DB=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass

Enhanced Test Condition

Min_Class_EV_Time_DB=	9.7	msec	9.7	9.7	9.7	6	Pass	20	Pass
Max_Class_EV_Time_DB=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Min_Mark_EV_Time_DB=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_DB=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Final_Mark_EV_Time_DB=	127	msec	127	127	127	6	Pass	256	Pass
Cl_Prb_Reset_Time_DB=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Test: class_response									
Class_3_Count=	1	****	1	1	1	1	Pass	1	Pass
Class_4_Count=	3	****	3	3	3	1	Pass	3	Pass
Class_5_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_6_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_7_Count=	5	****	5	5	5	1	Pass	5	Pass
Class_8_Count=	5	****	5	5	5	1	Pass	5	Pass
Class_2D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_2D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_3D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_3D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_5D_Count_A=	4	****	4	4	4	1	Pass	4	Pass
Class_5D_Count_B=	4	****	4	4	4	1	Pass	4	Pass
Max_SS_Class=	8	****	8	8	8	3	Pass	8	Pass
Max_DS_Class=	5	****	5	5	5	1	Pass	5	Pass
Init_Grant_Match=	1	****	1	1	1	1	Pass	1	Pass
2-Pair_Pairset=	0	****	0	0	0	0	Pass	2	Pass
PRI_4pr_Pairset=	12	****	12	12	12	1	Pass	12	Pass
Test: class_err									
Class_Ilim_A=	67.9	mA	67.9	67.9	67.9	51	Pass	100	Pass
Class_Ilim_B=	67.6	mA	67.6	67.6	67.6	51	Pass	100	Pass
Pwr_Cl_52_SS=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_52_DSA=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_52_DSB=	0	****	0	0	0	0	Pass	0	Pass
Class_Reset_SS=	1	****	1	1	1	1	Pass	1	Pass
Class_Reset_DS=	1	****	1	1	1	1	Pass	1	Pass
Mark_Ilim_A=	63	mA	63	63	63	5	Pass	100	Pass
Mark_Ilim_B=	65	mA	65	65	65	5	Pass	100	Pass
Inval_Sig_EV2_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV5_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV2_DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV2_DSB=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_DSB=	0	****	0	0	0	0	Pass	1	Pass
Test: pwrup_time									
Pwr_On_Time_Tpon_SS=	368.4	msec	368.4	368.4	368.4	144	Pass	400	Pass
Pwr_On_Time_Tpon_DSA=	346.9	msec	346.9	346.9	346.9	144	Pass	400	Pass
Pwr_On_Time_Tpon_DSB=	344.9	msec	344.9	344.9	344.9	144	Pass	400	Pass
Pwrup_Rise_Time_A=	142	usec	142	142	142	15	Pass	50000	Pass
Pwrup_Rise_Time_B=	136	usec	136	136	136	15	Pass	50000	Pass
Pwr_Stagger_Time_SS4=	0	msec	0	0	0	-1	Pass	75	Pass
Pwr_Stagger_Time_SS5=	0	msec	0	0	0	0	Pass	75	Pass

Enhanced Test Condition

Pwr_Stagger_Time_DS=	714.7	msec	714.7	714.7	714.7	0	Pass	1000	Pass
Test: pwrup_inrush									
Iinrush_min_Class_3=	423.7	mA	423.7	423.7	423.7	400	Pass	9999	Pass
Iinrush_min_Class_5=	864.4	mA	864.4	864.4	864.4	400	Pass	9999	Pass
Iinrush_min_Class_7=	864.2	mA	864.2	864.2	864.2	800	Pass	9999	Pass
Iinrush_min_Class_1D_A=	412.8	mA	412.8	412.8	412.8	400	Pass	9999	Pass
Iinrush_min_Class_1D_B=	415.8	mA	415.8	415.8	415.8	400	Pass	9999	Pass
Iinrush_4P_max_Class_3=	437	mA	437	437	437	0	Pass	450	Pass
Iinrush_4P_max2_Class_5=	881.8	mA	881.8	881.8	881.8	0	Pass	900	Pass
Iinrush_4P_max2_Class_7=	878.7	mA	878.7	878.7	878.7	0	Pass	900	Pass
Iinrush_2P_max_Class_3=	219.3	mA	219.3	219.3	219.3	0	Pass	450	Pass
Iinrush_2P_max2_Class_7=	441.4	mA	441.4	441.4	441.4	0	Pass	600	Pass
Iinrush_2p_max_C1_1D_A=	430.9	mA	430.9	430.9	430.9	0	Pass	450	Pass
Iinrush_2p_max_C1_1D_B=	430.3	mA	430.3	430.3	430.3	0	Pass	450	Pass
Tinrush_minPr_Class_3=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Tinrush_maxPr_Class_3=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Tinrush_minPr_Class_7=	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush_maxPr_Class_7=	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush_Class_1D_A=	60.35	msec	60.35	60.35	60.4	50	Pass	75	Pass
Tinrush_Class_1D_B=	60.35	msec	60.35	60.35	60.4	50	Pass	75	Pass
Delay_Inrush_Class_7=	60.35	msec	60.35	60.35	60.4	50	Pass	75	Pass
Delay_Inrush_Class_2D_A=	60.35	msec	60.35	60.35	60.4	50	Pass	75	Pass
Delay_Inrush_Class_2D_B=	60.35	msec	60.35	60.35	60.4	50	Pass	75	Pass
45ms_Pwr_Stat_Class_7=	1	****	1	1	1	1	Pass	1	Pass
45ms_Pwr_Stat_Class_2D_A=	1	****	1	1	1	1	Pass	1	Pass
45ms_Pwr_Stat_Class_2D_B=	1	****	1	1	1	1	Pass	1	Pass
Vinrush_Class_2D_A=	30.7	volts	30.7	30.7	30.7	30	Pass	60	Pass
Vinrush_Class_2D_B=	30.8	volts	30.8	30.8	30.8	30	Pass	60	Pass
Iinrush_16V_DS3A=	430.25	mA	430.25	430.25	430.3	60	Pass	450	Pass
Iinrush_16V_DS3B=	437.5	mA	437.5	437.5	437.5	60	Pass	450	Pass
Test: pwron_v									
Vpse_Max_Alt_A=	56.65	V	56.65	56.65	56.65	52	Pass	57	Pass
Vpse_Max_Alt_B=	56.63	V	56.63	56.63	56.63	52	Pass	57	Pass
Vpse_Min_Alt_A=	55.75	V	55.75	55.75	55.75	52	Pass	57	Pass
Vpse_Min_Alt_B=	55.58	V	55.58	55.58	55.58	52	Pass	57	Pass
Vport_PSE_diff=	0	mV	0	0	0	0	Pass	65	Pass
V_ripple_A=	191	mVp-p	191	191	191	0	Pass	500	Pass
V_ripple_B=	193	mVp-p	193	193	193	0	Pass	500	Pass
V_noise_A=	184	mVp-p	184	184	184	0	Pass	200	Pass
V_noise_B=	192	mVp-p	192	192	192	0	Pass	200	Pass
V_trans_A=	55.57	V	55.57	55.57	55.57	52	Pass	57	Pass
V_trans_B=	55.44	V	55.44	55.44	55.44	52	Pass	57	Pass
Test: pwron_pwrcap									
Max_Asgn_Class_SS=	8	****	8	8	8	1	Pass	8	Pass
Pcon_c1=	4.4	W	4.4	4.4	4.4	3.9	Pass	99.9	Pass
Icon_%_c1=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c2=	7.4	W	7.4	7.4	7.4	3.9	Pass	99.9	Pass
Icon_%_c2=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c3=	15	W	15	15	15	3.9	Pass	99.9	Pass
Icon_%_c3=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c4=	30.3	W	30.3	30.3	30.3	3.9	Pass	99.9	Pass

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Icon_% c4=	112.3	%	112.3	112.3	112.3	100	Pass	125	Pass
Pcon_c5=	49.1	W	49.1	49.1	49.1	3.9	Pass	99.9	Pass
Icon_% c5=	112.2	%	112.2	112.2	112.2	100	Pass	125	Pass
Pcon_c6=	64.5	W	64.5	64.5	64.5	3.9	Pass	99.9	Pass
Icon_% c6=	111.9	%	111.9	111.9	111.9	100	Pass	125	Pass
Pcon_c7=	81.1	W	81.1	81.1	81.1	3.9	Pass	99.9	Pass
Icon_% c7=	111.7	%	111.7	111.7	111.7	100	Pass	125	Pass
Pcon_c8=	96.3	W	96.3	96.3	96.3	3.9	Pass	99.9	Pass
Icon_% c8=	111.5	%	111.5	111.5	111.5	100	Pass	125	Pass
Type_N_Enable=	1	****	1	1	1	1	Pass	1	Pass
Pclass_2p_c5=	2	****	2	2	2	1	Pass	2	Pass
Max_Asgn_Class_DS=	5	****	5	5	5	1	Pass	5	Pass
Pcon_c1DA=	4.4	W	4.4	4.4	4	3.9	Pass	49.9	Pass
Icon_% c1DA=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c2DB=	7.5	W	7.5	7.5	8	3.9	Pass	49.9	Pass
Icon_% c2DB=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c3DA=	15.4	W	15.4	15.4	15	3.9	Pass	49.9	Pass
Icon_% c3DA=	112.3	%	112.3	112.3	112.3	100	Pass	125	Pass
Pcon_c4DB=	32.3	W	32.3	32.3	32	3.9	Pass	49.9	Pass
Icon_% c4DB=	112	%	112	112	112	100	Pass	125	Pass
Pcon_c5DA=	48	W	48	48	48	3.9	Pass	49.9	Pass
Icon_% c5DA=	111.4	%	111.4	111.4	111.4	100	Pass	125	Pass
Test: pwron_unbal									
pseP2pUnbal_c4A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c4B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_B=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_2p_max_SSA=	1266.8	mA	1266.8	1266.8	1266.8	0	Pass	1750	Pass
Ilim_2p_max_SSB=	1266.1	mA	1266.1	1266.1	1266.1	0	Pass	1750	Pass
Tlim_SS=	11.72	msec	11.72	11.72	11.7	6	Pass	75	Pass
Ilim_2p_max_DSA=	1267	mA	1267	1267	1267	0	Pass	1750	Pass
Ilim_2p_max_DSB=	1261	mA	1261	1261	1261	0	Pass	1750	Pass
Tlim_DSA=	12.11	msec	12.11	12.11	12.1	6	Pass	75	Pass
Tlim_DSB=	12.11	msec	12.11	12.11	12.1	6	Pass	75	Pass
Ilim_min_cAB3=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5=	580	mA	580	580	580	580	Pass	1750	Pass
Max_trans_c5=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB6=	720	mA	720	720	720	720	Pass	1750	Pass
Max_trans_c6=	1	****	1	1	1	1	Pass	1	Pass

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Ilim_min_cAB7=	850	mA	850	850	850	850	Pass	1750	Pass
Max_trans_c7=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB8=	1005	mA	1005	1005	1005	1005	Pass	1750	Pass
Max_trans_c8=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB3D=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4D=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5D=	990	mA	990	990	990	990	Pass	1750	Pass
Max_trans_c5D=	1	****	1	1	1	1	Pass	1	Pass
Vtrans_2p_A=	55.85	V	55.85	55.85	55.9	48.4	Pass	57	Pass
Vtrans_2p_B=	55.8	V	55.8	55.8	55.8	48.4	Pass	57	Pass
Iport_max_type4=	0	****	0	0	0	0	Pass	0	Pass
Ilps_type4=	0	****	0	0	0	0	Pass	0	Pass
Test: pwron_overld									
Ipeak_c1=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c3=	56.3	V	56.3	56.3	56.3	52	Pass	57	Pass
Ipeak_5%DC_c3=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c4=	56.17	V	56.17	56.17	56.2	52	Pass	57	Pass
Ipeak_5%DC_c4=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c5=	55.85	V	55.85	55.85	55.9	52	Pass	57	Pass
Ipeak_5%DC_c5=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c6=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c6=	55.63	V	55.63	55.63	55.6	52	Pass	57	Pass
Ipeak_5%DC_c6=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c7=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c7=	55.27	V	55.27	55.27	55.3	52	Pass	57	Pass
Ipeak_5%DC_c7=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c8=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c8=	54.95	V	54.95	54.95	55	52	Pass	57	Pass
Ipeak_5%DC_c8=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c1D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_valid									
Ihold_c3=	6	mA	6	6	6	4	Pass	9	Pass
Ihold_2p_c3A=	4	mA	4	4	4	2	Pass	5	Pass
Ihold_2p_c3B=	4	mA	4	4	4	2	Pass	5	Pass
Ihold_c5=	6	mA	6	6	6	4	Pass	14	Pass
Ihold_2p_c5A=	4	mA	4	4	4	2	Pass	7	Pass
Ihold_2p_c5B=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_c7=	6	mA	6	6	6	4	Pass	14	Pass
Ihold_2p_c7A=	4	mA	4	4	4	2	Pass	7	Pass
Ihold_2p_c7B=	4	mA	4	4	4	2	Pass	7	Pass
Ihold_2p_c2DA=	5	mA	5	5	5	2	Pass	7	Pass

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Ihold_2p_c2DB=	5	mA	5	5	5	2	Pass	7	Pass
LP_MPS_Tol_c3=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c5=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c7=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c2D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrndn									
Tmpdo_c3A=	365.2	msec	365.2	365.2	365.2	320	Pass	400	Pass
Tmpdo_c3B=	367.2	msec	367.2	367.2	367.2	320	Pass	400	Pass
Tmpdo_c5A=	367.2	msec	367.2	367.2	367.2	320	Pass	400	Pass
Tmpdo_c5B=	367.2	msec	367.2	367.2	367.2	320	Pass	400	Pass
Tmpdo_c7A=	367.2	msec	367.2	367.2	367.2	320	Pass	400	Pass
Tmpdo_c7B=	367.2	msec	367.2	367.2	367.2	320	Pass	400	Pass
Tmpdo_c2DA=	365.2	msec	365.2	365.2	365.2	320	Pass	400	Pass
4pr_Stat_c2DA=	1	****	1	1	1	0	Pass	1	Pass
Tmpdo_c2DB=	365.2	msec	365.2	365.2	365.2	320	Pass	400	Pass
4pr_Stat_c2DB=	1	****	1	1	1	0	Pass	1	Pass
Test: pwrndn_time									
Turnoff_time_Toff_A=	15.7	msec	15.7	15.7	15.7	0	Pass	500	Pass
Turnoff_time_Toff_B=	15.8	msec	15.8	15.8	15.8	0	Pass	500	Pass
Cout_A=	76	nF	76	76	76	0	Pass	520	Pass
Cout_B=	77.9	nF	77.9	77.9	77.9	0	Pass	520	Pass
Output_Rp_A=	87	Kohm	87	87	87	45	Pass	9999	Pass
Output_Rp_B=	85	Kohm	85	85	85	45	Pass	9999	Pass
Idle_time_SS=	1070.3	msec	1070.3	1070.3	1070.3	15	Pass	9999	Pass
Test: pwrndn_v									
Error_Delay_SS_A=	1016	msec	1016	1016	1016	750	Pass	9999	Pass
Error_Delay_SS_B=	1016	msec	1016	1016	1016	750	Pass	9999	Pass
Error_Delay_DS_A=	996	msec	996	996	996	750	Pass	9999	Pass
Error_Delay_DS_B=	1484	msec	1484	1484	1484	750	Pass	9999	Pass
Idle_Voff_SS_A=	1	V	1	1	1	0	Pass	2.8	Pass
Idle_Voff_SS_B=	1	V	1	1	1	0	Pass	2.8	Pass
Idle_Voff_DS_A=	0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass
Idle_Voff_DS_B=	0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass