

Enhanced Test Condition

PSE Conformance Test Suite

May 8 2024	2:11 PM
Port Count.....	1
Loop Count.....	1
PSE Tested: Unspecified Type-2 30W	



802.3at Conformance Report

version 5.3.14

Test Mode: **30 Watt PHY** report version 5.3.08

Sifos Interop Index*: **100%**

Error Log: None

Chassis ID: **192.168.221.105**

PSA-3000 Ports

TestLoop: **1**

	1-1	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v									
Open_Circuit_Det_Voc=	14.82	V	14.82	14.82	14.82	2.8	Pass	30	Pass
Peak_Det_Vvalid=	6.17	V	6.17	6.17	6.2	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.11	V	4.11	4.11	4.1	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	2.06	V	2.06	2.06	2.1	1	Pass	7.2	Pass
Detection_Slew=	0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	1	edges	1	1	1	1	Pass	9	Pass
Backoff_Voltage=	0.7	V	0.7	0.7	0.7	0	Pass	2.8	Pass
Non_802_Discr_? =	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	****	0	0	0	0	Pass	2	Pass
Test: det_i									
Init_Current_Isc=	0.16	mA	0.16	0.16	0.16	0	Pass	5	Pass
Det_Current_Isc=	0.15	mA	0.15	0.15	0.15	0	Pass	5	Pass
Test: det_range									
Rgood_Max=	30	Kohm	30	30	30	26	Pass	32	Pass
Rgood_Min=	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	30	Kohm	30	30	30	26	Pass	33	Pass
Cgood_Max=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	****	0	0	0	0	Pass	0	Pass
4Pair_Pwr_? =	0	****	0	0	0	0	Pass	0	Pass
Test: det_time									
Backoff_Time_Tdbo=	1016	msec	1016	1016	1016	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	1016	msec	1016	1016	1016	-1	Pass	1500	Pass
Backoff_Type=	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	375	msec	375	375	375	5	Pass	500	Pass
Total_Det_Time=	377	msec	377	377	377	5	Pass	1000	Pass
Test: det_rsource									

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PSE_Detect_Source=	1	****	1	1	1	0	Pass	1	Pass
Output_Impedance_Zout=	450	Kohm	450	450	450	45	Pass	2000	Pass
Test: class_v									
Vclass_Max=	19	V	19	19	19	15.5	Pass	20.5	Pass
Vclass_Min=	17.9	V	17.9	17.9	17.9	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	8.6	V	8.6	8.6	8.6	7	Pass	10	Pass
Mark_Voltage_Min=	8.5	V	8.5	8.5	8.5	7	Pass	10	Pass
Test: class_time									
Class_0_Count=	1	****	1	1	1	1	Pass	2	Pass
Class_Time_Tpdc=	10.8	msec	10.8	10.8	10.8	6	Pass	75	Pass
Class_4_Count=	2	****	2	2	2	2	Pass	2	Pass
Event1_Tcle1=	11.7	msec	11.7	11.7	11.7	6	Pass	30	Pass
Event2_Tcle2=	9.7	msec	9.7	9.7	9.7	6	Pass	30	Pass
Mark_Tme1=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Mark_Tme2=	255	msec	255	255	255	6	Pass	376	Pass
Mark_Tme1_LowI=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Test: class_err									
Class_lim=	66	mA	66	66	66	51	Pass	100	Pass
Pwr_Cl_lim=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	0	****	0	0	0	0	Pass	0	Pass
Treset=	211	msec	211	211	211	15	Pass	10000	Pass
Test: pwrup_time									
Power-On_Trise_c0=	159	usec	159	159	159	15	Pass	50000	Pass
Power-On_Tpon_c0=	273.4	msec	273.4	273.4	273.4	0	Pass	400	Pass
Power-On_Trise_c4=	176	usec	176	176	176	15	Pass	50000	Pass
Power-On_Tpon_c4=	293	msec	293	293	293	0	Pass	400	Pass
Test: pwrup_inrush									
Init_Iinrush=	435.75	mA	435.75	435.75	435.8	400	Pass	450	Pass
Max_Iinrush_c0=	428.38	mA	428.38	428.38	428.4	400	Pass	450	Pass
Max_Iinrush_c4=	428	mA	428	428	428	400	Pass	450	Pass
Min_Iinrush=	413.88	mA	413.88	413.88	413.9	400	Pass	450	Pass
Tinrush=	60	msec	60	60	60	50	Pass	75	Pass
Inrush_45m=	55.1	V	55.1	55.1	55.1	50	Pass	57	Pass

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Inrush_Voltage=	30.7	V	30.7	30.7	30.7	30	Pass	57	Pass
Inrush_Strategy_c0=	0	****	0	0	0	0	Pass	1	Pass
Inrush_Strategy_c4=	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v									
Vport_min=	54.3	V	54.3	54.3	54.3	50	Pass	57	Pass
Vport_max=	55.4	V	55.4	55.4	55.4	50	Pass	57	Pass
Vport_ripple=	24	mVpp	24	24	24	0	Pass	500	Pass
Vport_noise=	113	mVpp	113	113	113	0	Pass	200	Pass
Vtrans_min=	54.1	V	54.1	54.1	54.1	50	Pass	57	Pass
Vtrans_max=	55.5	V	55.5	55.5	55.5	50	Pass	57	Pass
Test: pwron_pwr cap									
Pcon_c0=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c0=	134.6	%	134.6	134.6	134.6	100	Pass	9999	Pass
Pcon_c1=	5.7	W	5.7	5.7	5.7	3.9	Pass	22.7	Pass
Icon_%_c1=	147.2	%	147.2	147.2	147.2	100	Pass	9999	Pass
Pcon_c2=	8.2	W	8.2	8.2	8.2	6.8	Pass	22.7	Pass
Icon_%_c2=	122.3	%	122.3	122.3	122.3	100	Pass	9999	Pass
Pcon_c3=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c3=	134.6	%	134.6	134.6	134.6	100	Pass	9999	Pass
Pcon_c4=	34	W	34	34	34	28.7	Pass	38.9	Pass
Icon_%_c4=	117	%	117	117	117	100	Pass	9999	Pass
Type-2_Enable=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_Peak_1=	492.8	mA	492.8	492.8	492.8	0	Pass	1750	Pass
Ilim_Min_1=	403.2	mA	403.2	403.2	403.2	400	Pass	1750	Pass
Tlim_1=	61.7	msec	61.7	61.7	61.7	10	Pass	9999	Pass
Vlim_1=	54.5	V	54.5	54.5	54.5	50	Pass	57	Pass
Ilim_Max_1=	492.8	mA	492.8	492.8	492.8	400	Pass	1750	Pass
Ktran_lo_1=	108.9	%	108.9	108.9	108.9	92.4	Pass	115	Pass
Ilim_Peak_2=	845.5	mA	845.5	845.5	845.5	0	Pass	1750	Pass
Ilim_Min_2=	686.6	mA	686.6	686.6	686.6	683	Pass	1750	Pass
Tlim_2=	19.1	msec	19.1	19.1	19.1	10	Pass	75	Pass
Vlim_2=	53.9	V	53.9	53.9	53.9	50	Pass	57	Pass
Ilim_Max_2=	834.3	mA	834.3	834.3	834.3	683	Pass	1750	Pass
Ktran_lo_2=	107.7	%	107.7	107.7	107.7	92.4	Pass	115	Pass

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Test: pwrn_overld									
%Ipeak_1=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_1=	54.6	V	54.6	54.6	54.6	50	Pass	57	Pass
Vport_5%DC_1=	54.6	V	54.6	54.6	54.6	50	Pass	57	Pass
%Ipeak_2=	109.9	%	109.9	109.9	109.9	100	Pass	125	Pass
Vport_Ipeak_2=	54	V	54	54	54	50	Pass	57	Pass
Vport_5%DC_2=	54	V	54	54	54	50	Pass	57	Pass
Test: mps_dc_valid									
Min_Valid_Time_Tmps=	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn									
Min_Valid_I_hold=	6	mA	6	6	6	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	364	msec	364	364	364	300	Pass	400	Pass
Test: pwrn_overld									
Icut_1=	377.5	mA	377.5	377.5	377.5	-1	Pass	1750	Pass
Tcut_1=	61.7	msec	61.7	61.7	61.7	50	Pass	9999	Pass
Isoft_1=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_1=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Icut_2=	640.5	mA	640.5	640.5	640.5	-1	Pass	1750	Pass
Tcut_2=	61.6	msec	61.6	61.6	61.6	10	Pass	9999	Pass
Isoft_2=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrn_time									
Turn-Off_Time_Toff=	57.6	msec	57.6	57.6	57.6	0	Pass	500	Pass
Output_Cap_Cout=	0.0644	uF	0.0644	0.0644	0.06	-1	Pass	0.52	Pass
Output_Load_Rp=	9999	Kohm	9999	9999	9999	45	Pass	50000	Pass
Test: pwrn_v									
Avg_Idle_Voff=	0.6	V	0.6	0.6	0.6	0	Pass	2.8	Pass
Error_Delay_Ted=	3523.4	msec	3523.4	3523.4	3523	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	18.6	V	18.6	18.6	18.6	0	Pass	20.5	Pass
Test Port Model Number:	3202								
Test Port Hardware Version:	11								
Test Port Firmware Version:	4.19 lc19								