DIGITUS®

Application Report
PSA-3402 – February 2025

SIFOs 802.3bt Conformance Report, v 5.4.10, DN-651141

1 Sifos Technologies

Sifos Technologies provides a one-box solution to facilitate complete testing and analysis of Power Sourcing Equipment (PSE) behaviors and overall compliance to the IEEE 802.3bt specification. The PSE Conformance Test Suite serves as a virtual industry standard for PSE specification compliance. SIFOS test coverage exceeds 95% of 802.3bt PSE PICS

2 Enhanced Test Conditions

In additional to just running the basic conformance testing on an individual port and to better recreate the system-level environment, ASSMANN individually tests all ports of its PSE controller devices while having the background ports running at various PoE-application conditions. ASSMANN further, repeats testing conditions over extended periods of time to ensure performance is consistent after multiple hours, days and/or continued operation.

PSE Conformance Test Suite

February 20 2025	4:37 PM
Port Count.....	1
Loop Count.....	1

PSE Tested: **Results Type-4**

Sifos Technologies

802.3bt 4Pr Conformance Report

34020093A *version* *5.4.10*

PSE Type: **4 MDI-X+MDI** *report version 5.4.02*

Safety Index*: **100%** **Interop Index*:** **100%**

Error Log: None

Chassis ID: 192.168.221.105	PSA-3402 Ports					Low	P/F	High	P/F
TestLoop: 1	1-1	UNITS	Min	Max	Average	Limit		Limit	
Test: det_v									
Open_Circuit_Voc_A=	14.7	volts	14.7	14.7	14.7	0	Pass	30	Pass
Open_Circuit_Voc_B=	15	volts	15	15	15	0	Pass	30	Pass
Backoff_Voltage_A=	0.9	volts	0.9	0.9	0.9	0	Pass	2.8	Pass
Backoff_Voltage_B=	1	volts	1	1	1	0	Pass	2.8	Pass
Backoff_Voltage_Ss=	1	volts	1	1	1	0	Pass	2.8	Pass
Max_Det_Step_V_A=	7.04	volts	7.04	7.04	7.04	3.8	Pass	10	Pass
Max_Det_Step_V_B=	7.05	volts	7.05	7.05	7.05	3.8	Pass	10	Pass
Min_Det_Step_V_A=	3.67	volts	3.67	3.67	3.67	2.8	Pass	9	Pass
Min_Det_Step_V_B=	3.68	volts	3.68	3.68	3.68	2.8	Pass	9	Pass
Det_Step_Changes_A=	1	****	1	1	1	1	Pass	9	Pass
Det_Step_Changes_B=	1	****	1	1	1	1	Pass	9	Pass
Min_Step_DV_A=	1.73	volts	1.73	1.73	1.73	1	Pass	7.2	Pass
Min_Step_DV_B=	1.73	volts	1.73	1.73	1.73	1	Pass	7.2	Pass
Pre-Det_CC_Step_V_A=	3.63	volts	3.63	3.63	3.63	0	Pass	10	Pass
Pre-Det_CC_Step_V_B=	0	volts	0	0	0	0	Pass	10	Pass
Test: det_cc									
Presumed_CC_DET_SEQ=	1	****	1	1	1	0	Pass	3	Pass
Conn_Chk_SS_V_A=	8.88	volts	8.88	8.88	8.88	2.8	Pass	10	Pass
Conn_Chk_SS_V_B=	8.93	volts	8.93	8.93	8.93	2.8	Pass	10	Pass
Conn_Chk_DS_V_A=	3.65	volts	3.65	3.65	3.65	2.8	Pass	10	Pass
Conn_Chk_DS_V_B=	6.26	volts	6.26	6.26	6.26	2.8	Pass	10	Pass
High_Signature_CC_A=	1	****	1	1	1	1	Pass	1	Pass
High_Signature_CC_B=	1	****	1	1	1	1	Pass	1	Pass
4Pair_Start_Fail=	0	****	0	0	0	0	Pass	0	Pass
Test: det_i									
Isc_Init_A=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Init_B=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Det_A=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Isc_Det_B=	0.22	mA	0.22	0.22	0.22	0	Pass	5	Pass
Det_Slew_A=	0.004	V/usec	0.004	0.004	0.004	0	Pass	0.1	Pass
Det_Slew_B=	0.004	V/usec	0.004	0.004	0.004	0	Pass	0.1	Pass
Test: det_range									
Rgood_Max_Single=	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood_Min_Single=	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Single=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rgood_Max_Dual_A=	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood_Max_Dual_B=	30	Kohm	30	30	30	27	Pass	32	Pass
Rgood_Min_Dual_A=	17	Kohm	17	17	17	16	Pass	19	Pass
Rgood_Min_Dual_B=	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Dual_A=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Cgood_Max_Dual_B=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass

Enhanced Test Condition

Test: det_time									
Detect_Time_Tdet_A=	367.2	msec	367.2	367.2	367.2	0	Pass	500	Pass
Detect_Time_Tdet_B=	375	msec	375	375	375	0	Pass	500	Pass
Backoff_Time_SS=	1019	msec	1019	1019	1019	15	Pass	9999	Pass
Backoff_Time_DS=	1464	msec	1464	1464	1464	15	Pass	9999	Pass
Det2Det_Time=	238.3	msec	238.3	238.3	238.3	0	Pass	400	Pass
Test: det_rsource									
PSE_Detect_Source=	1	****	1	1	1	0	Pass	1	Pass
PSE_Source_Zout_A=	300	Kohm	300	300	300	45	Pass	300	Pass
PSE_Source_Zout_B=	300	Kohm	300	300	300	45	Pass	300	Pass
Test: cc_response									
Single_Sig_Response=	1	****	1	1	1	1	Pass	1	Pass
Dual_Sig_Response=	1	****	1	1	1	1	Pass	1	Pass
2Pair_PD_A=	1	****	1	1	1	0	Pass	1	Pass
2Pair_PD_B=	1	****	1	1	1	0	Pass	1	Pass
Test: class_v									
Vclass_max_SS=	19	volts	19	19	19	15.5	Pass	20.5	Pass
Vclass_min_SS=	18.6	volts	18.6	18.6	18.6	15.5	Pass	20.5	Pass
Vmark_SS=	8.9	volts	8.9	8.9	8.9	7	Pass	10	Pass
Vreset_SS=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vclass_max_DSA=	18.7	volts	18.7	18.7	18.7	15.5	Pass	20.5	Pass
Vclass_max_DSB=	18.9	volts	18.9	18.9	18.9	15.5	Pass	20.5	Pass
Vclass_min_DSA=	18.4	volts	18.4	18.4	18.4	15.5	Pass	20.5	Pass
Vclass_min_DSB=	18.6	volts	18.6	18.6	18.6	15.5	Pass	20.5	Pass
Vmark_DSA=	8.7	volts	8.7	8.7	8.7	7	Pass	10	Pass
Vmark_DSB=	8.8	volts	8.8	8.8	8.8	7	Pass	10	Pass
Vreset_DSA=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vreset_DSB=	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Test: class_time									
Class_Probe_SS=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_7_SS=	5	Events	5	5	5	1	Pass	5	Pass
Long_EV1_Time_SS=	93.7	msec	93.7	93.7	93.7	88	Pass	105	Pass
Min_Class_EV_Time_SS=	9.7	msec	9.7	9.7	9.7	6	Pass	20	Pass
Max_Class_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Min_Mark_EV_Time_SS=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_SS=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Min_Mark_EV_LowI_SS=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Final_Mark_EV_Time_SS=	121.1	msec	121.1	121.1	121.1	6	Pass	256	Pass
Cl_Prb_Reset_Time_SS=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DA=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DA=	4	Events	4	4	4	1	Pass	4	Pass
Long_EV1_Time_DA=	93.7	msec	93.7	93.7	93.7	88	Pass	105	Pass
Min_Class_EV_Time_DA=	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Max_Class_EV_Time_DA=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Min_Mark_EV_Time_DA=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_DA=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Final_Mark_EV_Time_DA=	123.1	msec	123.1	123.1	123.1	6	Pass	256	Pass
Cl_Prb_Reset_Time_DA=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DB=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DB=	4	Events	4	4	4	1	Pass	4	Pass
Long_EV1_Time_DB=	93.7	msec	93.7	93.7	93.7	88	Pass	105	Pass

Min_Class_EV_Time_DB=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Max_Class_EV_Time_DB=	11.8	msec	11.8	11.8	11.8	6	Pass	20	Pass
Min_Mark_EV_Time_DB=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Max_Mark_EV_Time_DB=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Final_Mark_EV_Time_DB=	123.1	msec	123.1	123.1	123.1	6	Pass	256	Pass
Cl_Prb_Reset_Time_DB=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Test: class_response									
Class_3_Count=	1	****	1	1	1	1	Pass	1	Pass
Class_4_Count=	3	****	3	3	3	1	Pass	3	Pass
Class_5_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_6_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_7_Count=	5	****	5	5	5	1	Pass	5	Pass
Class_8_Count=	5	****	5	5	5	1	Pass	5	Pass
Class_2D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_2D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_3D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_3D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_5D_Count_A=	4	****	4	4	4	1	Pass	4	Pass
Class_5D_Count_B=	4	****	4	4	4	1	Pass	4	Pass
Max_SS_Class=	8	****	8	8	8	3	Pass	8	Pass
Max_DS_Class=	5	****	5	5	5	1	Pass	5	Pass
Init_Grant_Match=	1	****	1	1	1	1	Pass	1	Pass
2-Pair_Pairset=	0	****	0	0	0	0	Pass	2	Pass
PRI_4pr_Pairset=	12	****	12	12	12	1	Pass	12	Pass
Test: class_err									
Class_Ilim_A=	66.1	mA	66.1	66.1	66.1	51	Pass	100	Pass
Class_Ilim_B=	65.7	mA	65.7	65.7	65.7	51	Pass	100	Pass
Pwr_Cl_52_SS=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_52_DSA=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_52_DSB=	0	****	0	0	0	0	Pass	0	Pass
Class_Reset_SS=	1	****	1	1	1	1	Pass	1	Pass
Class_Reset_DS=	1	****	1	1	1	1	Pass	1	Pass
Mark_Ilim_A=	64	mA	64	64	64	5	Pass	100	Pass
Mark_Ilim_B=	61	mA	61	61	61	5	Pass	100	Pass
Inval_Sig_EV2_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV5_SS=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV2_DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV2_DSB=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval_Sig_EV4_DSB=	0	****	0	0	0	0	Pass	1	Pass
Test: pwrup_time									
Pwr_On_Time_Tpon_SS=	364.4	msec	364.4	364.4	364.4	144	Pass	400	Pass
Pwr_On_Time_Tpon_DSA=	339.1	msec	339.1	339.1	339.1	144	Pass	400	Pass
Pwr_On_Time_Tpon_DSB=	341	msec	341	341	341	144	Pass	400	Pass
Pwrup_Rise_Time_A=	133	usec	133	133	133	15	Pass	50000	Pass
Pwrup_Rise_Time_B=	134	usec	134	134	134	15	Pass	50000	Pass
Pwr_Stagger_Time_SS4=	0	msec	0	0	0	-1	Pass	75	Pass
Pwr_Stagger_Time_SS5=	0	msec	0	0	0	0	Pass	75	Pass

Enhanced Test Condition

Pwr_Stagger_Time_DS=	701.9	msec	701.9	701.9	701.9	0	Pass	1000	Pass
Test: pwrup_inrush									
Iinrush_min_Class_3=	419.2	mA	419.2	419.2	419.2	400	Pass	9999	Pass
Iinrush_min_Class_5=	867	mA	867	867	867	400	Pass	9999	Pass
Iinrush_min_Class_7=	866.6	mA	866.6	866.6	866.6	800	Pass	9999	Pass
Iinrush_min_Class_1D_A=	417.7	mA	417.7	417.7	417.7	400	Pass	9999	Pass
Iinrush_min_Class_1D_B=	415.5	mA	415.5	415.5	415.5	400	Pass	9999	Pass
Iinrush_4P_max_Class_3=	434.9	mA	434.9	434.9	434.9	0	Pass	450	Pass
Iinrush_4P_max2_Class_5=	884.5	mA	884.5	884.5	884.5	0	Pass	900	Pass
Iinrush_4P_max2_Class_7=	884.5	mA	884.5	884.5	884.5	0	Pass	900	Pass
Iinrush_2P_max_Class_3=	220	mA	220	220	220	0	Pass	450	Pass
Iinrush_2P_max2_Class_7=	444.3	mA	444.3	444.3	444.3	0	Pass	600	Pass
Iinrush_2p_max_Cl_1D_A=	435.9	mA	435.9	435.9	435.9	0	Pass	450	Pass
Iinrush_2p_max_Cl_1D_B=	432.6	mA	432.6	432.6	432.6	0	Pass	450	Pass
Tinrush_minPr_Class_3=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Tinrush_maxPr_Class_3=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Tinrush_minPr_Class_7=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Tinrush_maxPr_Class_7=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Tinrush_Class_1D_A=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Tinrush_Class_1D_B=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Delay_Inrush_Class_7=	58.4	msec	58.4	58.4	58.4	50	Pass	75	Pass
Delay_Inrush_Class_2D_A=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
Delay_Inrush_Class_2D_B=	59.18	msec	59.18	59.18	59.2	50	Pass	75	Pass
45ms_Pwr_Stat_Class_7=	1	****	1	1	1	1	Pass	1	Pass
45ms_Pwr_Stat_Class_2D_A=	1	****	1	1	1	1	Pass	1	Pass
45ms_Pwr_Stat_Class_2D_B=	1	****	1	1	1	1	Pass	1	Pass
Vinrush_Class_2D_A=	30.9	volts	30.9	30.9	30.9	30	Pass	60	Pass
Vinrush_Class_2D_B=	30.8	volts	30.8	30.8	30.8	30	Pass	60	Pass
Iinrush_16V_DS3A=	137.7	mA	137.7	137.7	137.7	60	Pass	450	Pass
Iinrush_16V_DS3B=	429.5	mA	429.5	429.5	429.5	60	Pass	450	Pass
Test: pwron_v									
Vpse_Max_Alt_A=	55.35	V	55.35	55.35	55.35	52	Pass	57	Pass
Vpse_Max_Alt_B=	55.38	V	55.38	55.38	55.38	52	Pass	57	Pass
Vpse_Min_Alt_A=	54.5	V	54.5	54.5	54.5	52	Pass	57	Pass
Vpse_Min_Alt_B=	54.15	V	54.15	54.15	54.15	52	Pass	57	Pass
Vport_PSE_diff=	0	mV	0	0	0	0	Pass	65	Pass
V_ripple_A=	35	mVp-p	35	35	35	0	Pass	500	Pass
V_ripple_B=	37	mVp-p	37	37	37	0	Pass	500	Pass
V_noise_A=	173	mVp-p	173	173	173	0	Pass	200	Pass
V_noise_B=	165	mVp-p	165	165	165	0	Pass	200	Pass
V_trans_A=	54.38	V	54.38	54.38	54.38	52	Pass	57	Pass
V_trans_B=	53.97	V	53.97	53.97	53.97	52	Pass	57	Pass
Test: pwron_pwrcap									
Max_Asgn_Class_SS=	8	****	8	8	8	1	Pass	8	Pass
Pcon_c1=	4.4	W	4.4	4.4	4.4	3.9	Pass	99.9	Pass
Icon_%_c1=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c2=	7.4	W	7.4	7.4	7.4	3.9	Pass	99.9	Pass
Icon_%_c2=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c3=	15	W	15	15	15	3.9	Pass	99.9	Pass
Icon_%_c3=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c4=	30.3	W	30.3	30.3	30.3	3.9	Pass	99.9	Pass

Icon_%_c4=	112.3	%	112.3	112.3	112.3	100	Pass	125	Pass
Pcon_c5=	49.3	W	49.3	49.3	49.3	3.9	Pass	99.9	Pass
Icon_%_c5=	112.1	%	112.1	112.1	112.1	100	Pass	125	Pass
Pcon_c6=	64.9	W	64.9	64.9	64.9	3.9	Pass	99.9	Pass
Icon_%_c6=	111.9	%	111.9	111.9	111.9	100	Pass	125	Pass
Pcon_c7=	81.8	W	81.8	81.8	81.8	3.9	Pass	99.9	Pass
Icon_%_c7=	111.5	%	111.5	111.5	111.5	100	Pass	125	Pass
Pcon_c8=	97.3	W	97.3	97.3	97.3	3.9	Pass	99.9	Pass
Icon_%_c8=	111.2	%	111.2	111.2	111.2	100	Pass	125	Pass
Type_N_Enable=	1	****	1	1	1	1	Pass	1	Pass
Pclass_2p_c5=	2	****	2	2	2	1	Pass	2	Pass
Max_Asgn_Class_DS=	5	****	5	5	5	1	Pass	5	Pass
Pcon_c1DA=	4.4	W	4.4	4.4	4	3.9	Pass	49.9	Pass
Icon_%_c1DA=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c2DB=	7.5	W	7.5	7.5	8	3.9	Pass	49.9	Pass
Icon_%_c2DB=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c3DA=	15.5	W	15.5	15.5	16	3.9	Pass	49.9	Pass
Icon_%_c3DA=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c4DB=	32.5	W	32.5	32.5	33	3.9	Pass	49.9	Pass
Icon_%_c4DB=	111.8	%	111.8	111.8	111.8	100	Pass	125	Pass
Pcon_c5DA=	48.6	W	48.6	48.6	49	3.9	Pass	49.9	Pass
Icon_%_c5DA=	111.4	%	111.4	111.4	111.4	100	Pass	125	Pass
Test: pwron_unbal									
pseP2pUnbal_c4A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c4B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_B=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_2p_max_SSA=	1267	mA	1267	1267	1267	0	Pass	1750	Pass
Ilim_2p_max_SSB=	1279	mA	1279	1279	1279	0	Pass	1750	Pass
Tlim_SS=	11.72	msec	11.72	11.72	11.7	6	Pass	75	Pass
Ilim_2p_max_DSA=	1265	mA	1265	1265	1265	0	Pass	1750	Pass
Ilim_2p_max_DSB=	1265	mA	1265	1265	1265	0	Pass	1750	Pass
Tlim_DSA=	12.11	msec	12.11	12.11	12.1	6	Pass	75	Pass
Tlim_DSB=	12.11	msec	12.11	12.11	12.1	6	Pass	75	Pass
Ilim_min_cAB3=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5=	580	mA	580	580	580	580	Pass	1750	Pass
Max_trans_c5=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB6=	720	mA	720	720	720	720	Pass	1750	Pass
Max_trans_c6=	1	****	1	1	1	1	Pass	1	Pass

Enhanced Test Condition

Ilim_min_cAB7=	850	mA	850	850	850	850	Pass	1750	Pass
Max_trans_c7=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB8=	1005	mA	1005	1005	1005	1005	Pass	1750	Pass
Max_trans_c8=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB3D=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4D=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5D=	990	mA	990	990	990	990	Pass	1750	Pass
Max_trans_c5D=	1	****	1	1	1	1	Pass	1	Pass
Vtrans_2p_A=	54.08	V	54.08	54.08	54.1	48.4	Pass	57	Pass
Vtrans_2p_B=	53.9	V	53.9	53.9	53.9	48.4	Pass	57	Pass
Iport_max_type4=	0	****	0	0	0	0	Pass	0	Pass
Ilps_type4=	0	****	0	0	0	0	Pass	0	Pass
Test: pwron_overld									
Ipeak_c1=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c3=	55.2	V	55.2	55.2	55.2	52	Pass	57	Pass
Ipeak_5%DC_c3=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c4=	54.9	V	54.9	54.9	54.9	52	Pass	57	Pass
Ipeak_5%DC_c4=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c5=	54.58	V	54.58	54.58	54.6	52	Pass	57	Pass
Ipeak_5%DC_c5=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c6=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c6=	54.42	V	54.42	54.42	54.4	52	Pass	57	Pass
Ipeak_5%DC_c6=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c7=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c7=	54.15	V	54.15	54.15	54.2	52	Pass	57	Pass
Ipeak_5%DC_c7=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c8=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c8=	53.85	V	53.85	53.85	53.9	52	Pass	57	Pass
Ipeak_5%DC_c8=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c1D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_valid									
Ihold_c3=	5	mA	5	5	5	4	Pass	9	Pass
Ihold_2p_c3A=	3	mA	3	3	3	2	Pass	5	Pass
Ihold_2p_c3B=	3	mA	3	3	3	2	Pass	5	Pass
Ihold_c5=	5	mA	5	5	5	4	Pass	14	Pass
Ihold_2p_c5A=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c5B=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_c7=	5	mA	5	5	5	4	Pass	14	Pass
Ihold_2p_c7A=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c7B=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c2DA=	4	mA	4	4	4	2	Pass	7	Pass

Ihold_2p_c2DB=	4	mA	4	4	4	2	Pass	7	Pass
LP_MPS_Tol_c3=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c5=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c7=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c2D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn									
Tmpdo_c3A=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c3B=	359.4	msec	359.4	359.4	359.4	320	Pass	400	Pass
Tmpdo_c5A=	359.4	msec	359.4	359.4	359.4	320	Pass	400	Pass
Tmpdo_c5B=	359.4	msec	359.4	359.4	359.4	320	Pass	400	Pass
Tmpdo_c7A=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c7B=	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
Tmpdo_c2DA=	359.4	msec	359.4	359.4	359.4	320	Pass	400	Pass
4pr_Stat_c2DA=	1	****	1	1	1	0	Pass	1	Pass
Tmpdo_c2DB=	359.4	msec	359.4	359.4	359.4	320	Pass	400	Pass
4pr_Stat_c2DB=	1	****	1	1	1	0	Pass	1	Pass
Test: pwrdn_time									
Turnoff_time_Toff_A=	16.7	msec	16.7	16.7	16.7	0	Pass	500	Pass
Turnoff_time_Toff_B=	16.8	msec	16.8	16.8	16.8	0	Pass	500	Pass
Cout_A=	70.5	nF	70.5	70.5	70.5	0	Pass	520	Pass
Cout_B=	73.1	nF	73.1	73.1	73.1	0	Pass	520	Pass
Output_Rp_A=	105	Kohm	105	105	105	45	Pass	9999	Pass
Output_Rp_B=	101	Kohm	101	101	101	45	Pass	9999	Pass
Idle_time_SS=	992.2	msec	992.2	992.2	992.2	15	Pass	9999	Pass
Test: pwrdn_v									
Error_Delay_SS_A=	1035	msec	1035	1035	1035	750	Pass	9999	Pass
Error_Delay_SS_B=	1035	msec	1035	1035	1035	750	Pass	9999	Pass
Error_Delay_DS_A=	1641	msec	1641	1641	1641	750	Pass	9999	Pass
Error_Delay_DS_B=	1172	msec	1172	1172	1172	750	Pass	9999	Pass
Idle_Voff_SS_A=	1	V	1	1	1	0	Pass	2.8	Pass
Idle_Voff_SS_B=	1	V	1	1	1	0	Pass	2.8	Pass
Idle_Voff_DS_A=	0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass
Idle_Voff_DS_B=	0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass